

PRIOR ART

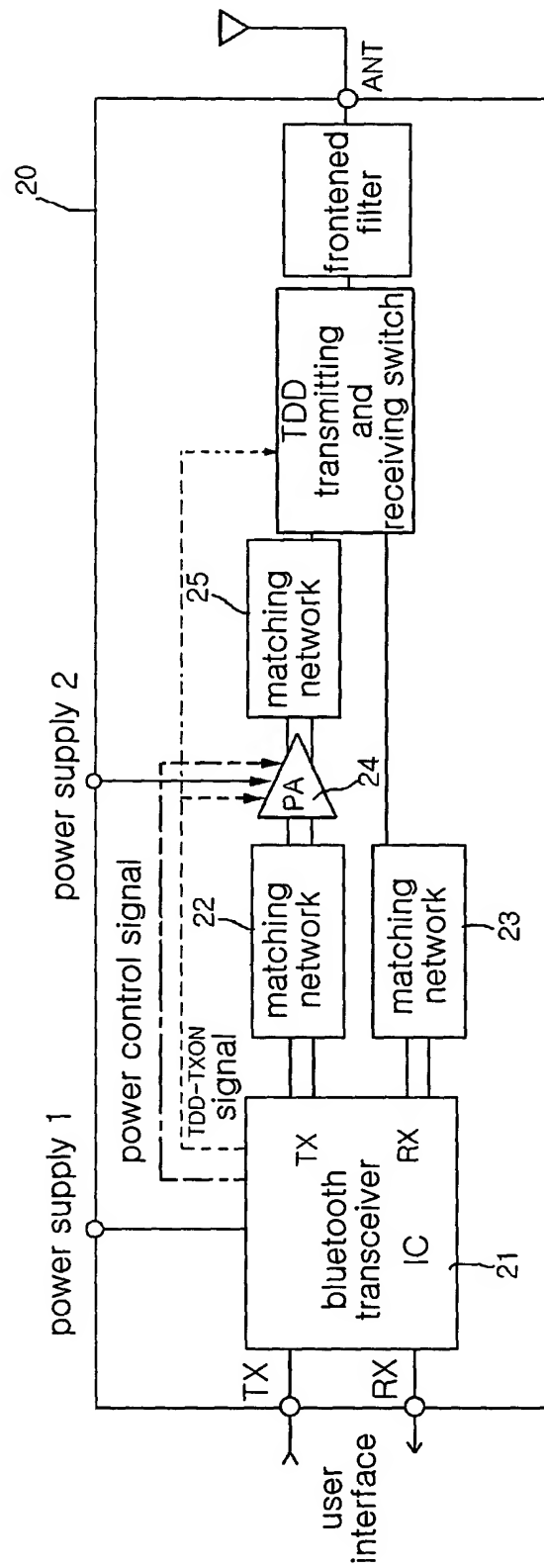
FIG. 1

TIME-DIVISION-DUPLEXING TYPE POWER AMPLIFICATION MODULE

Inventor: Ja Young PARK

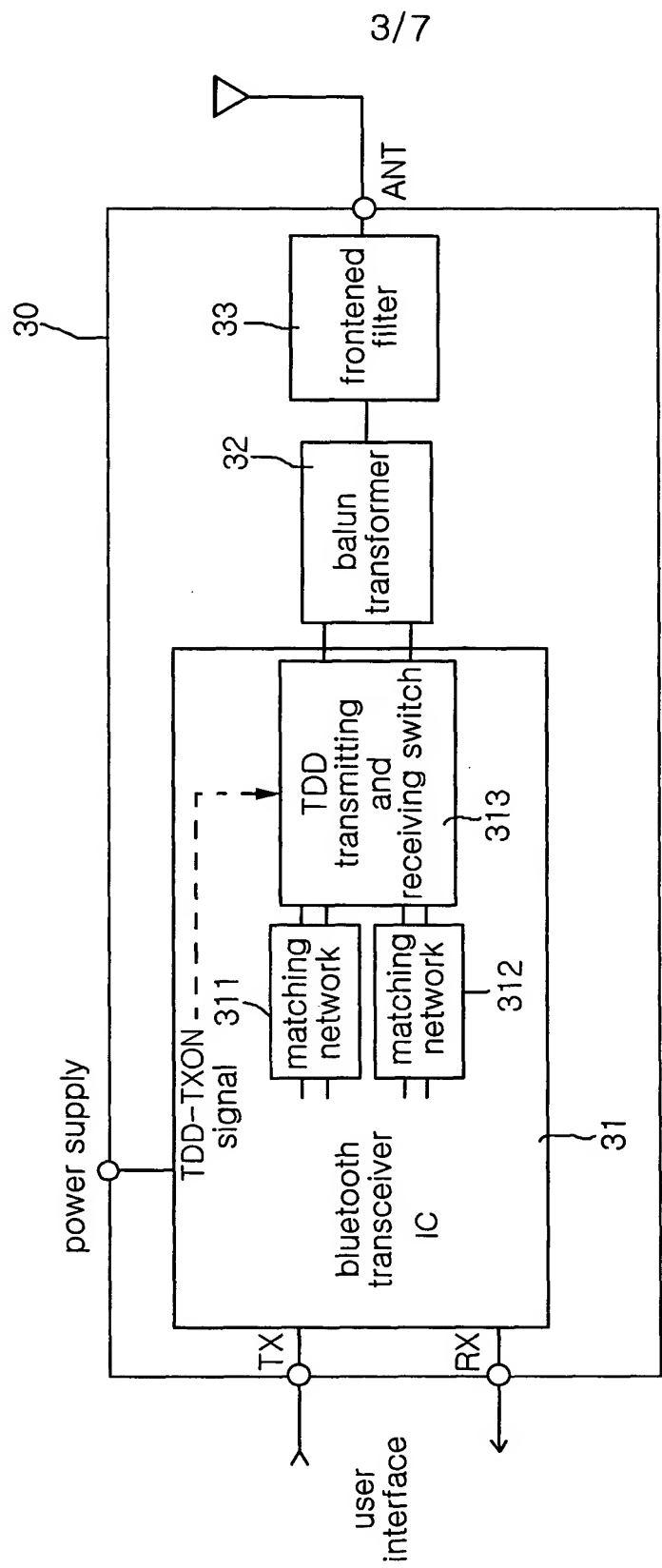
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PRIOR ART

FIG. 2



PRIOR ART

FIG. 3

TIME-DIVISION-DUPLEXING TYPE POWER AMPLIFICATION MODULE

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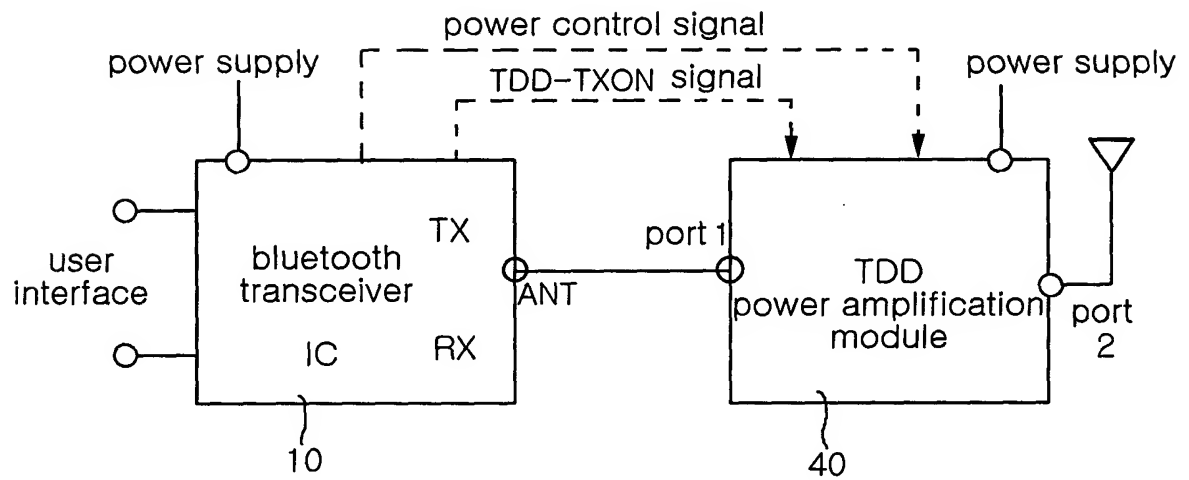


FIG. 4

TIME-DIVISION-DUPLEXING TYPE POWER AMPLIFICATION MODULE

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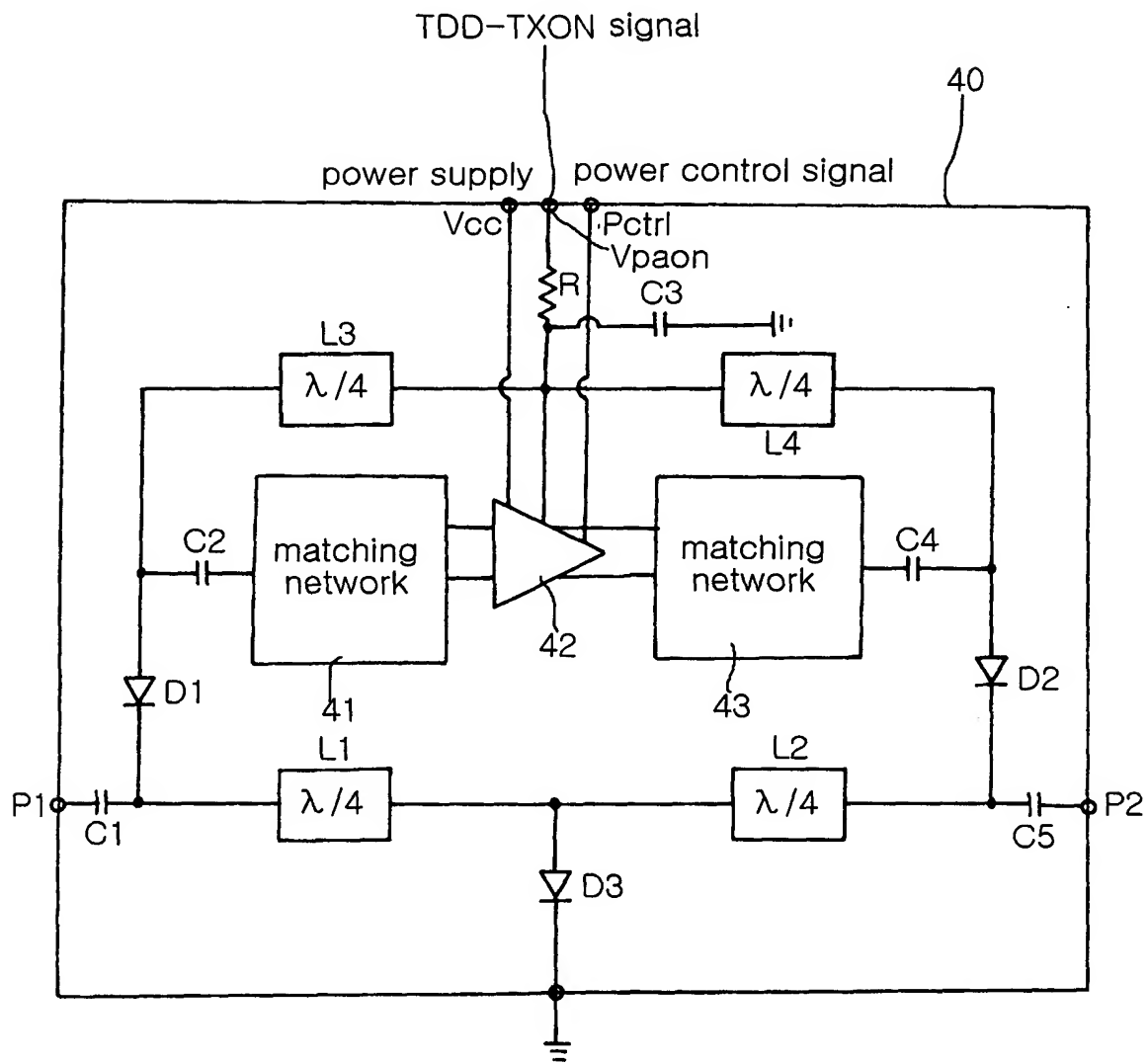


FIG. 5

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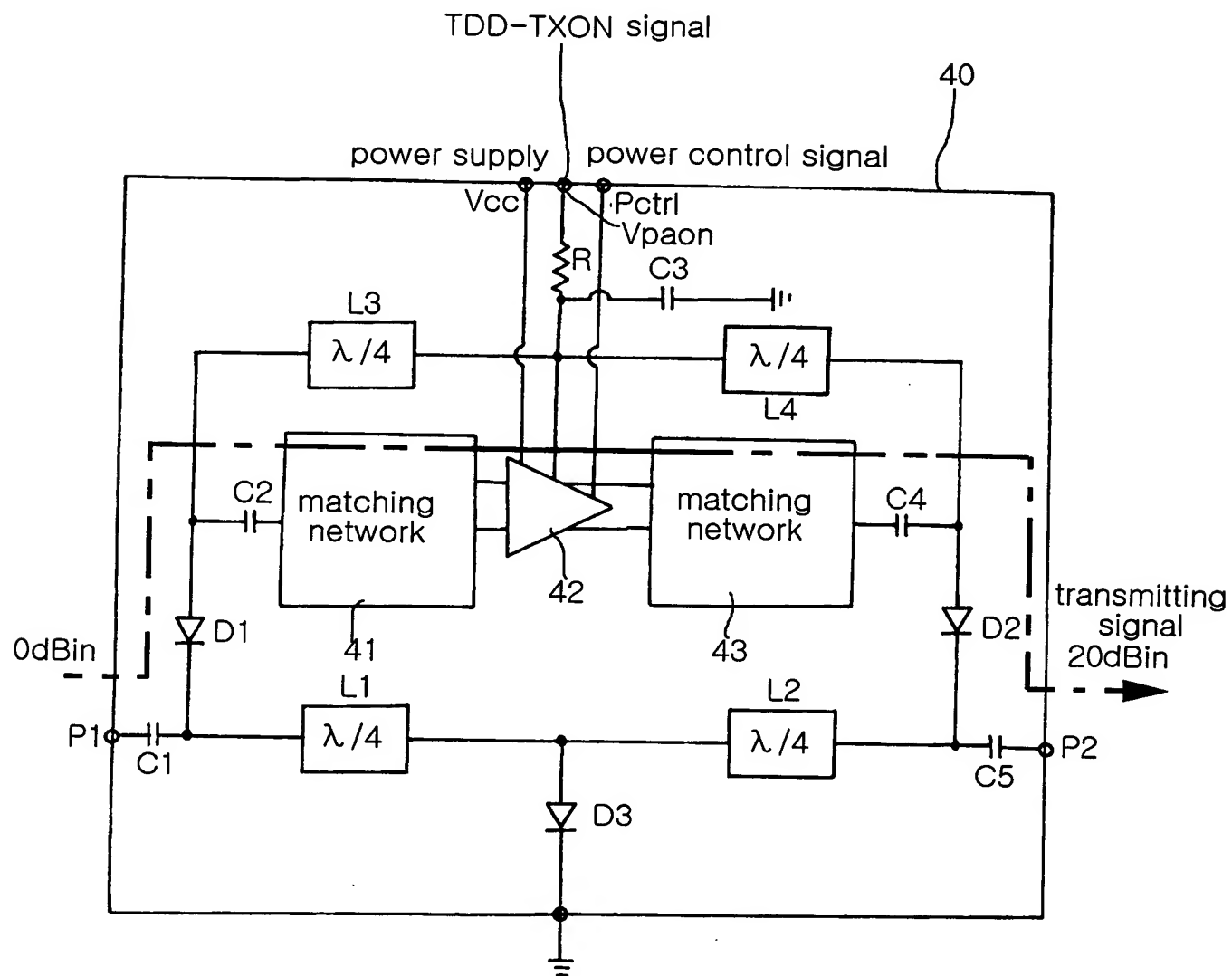


FIG. 6A

TIME-DIVISION-DUPLEXING TYPE POWER AMPLIFICATION MODULE

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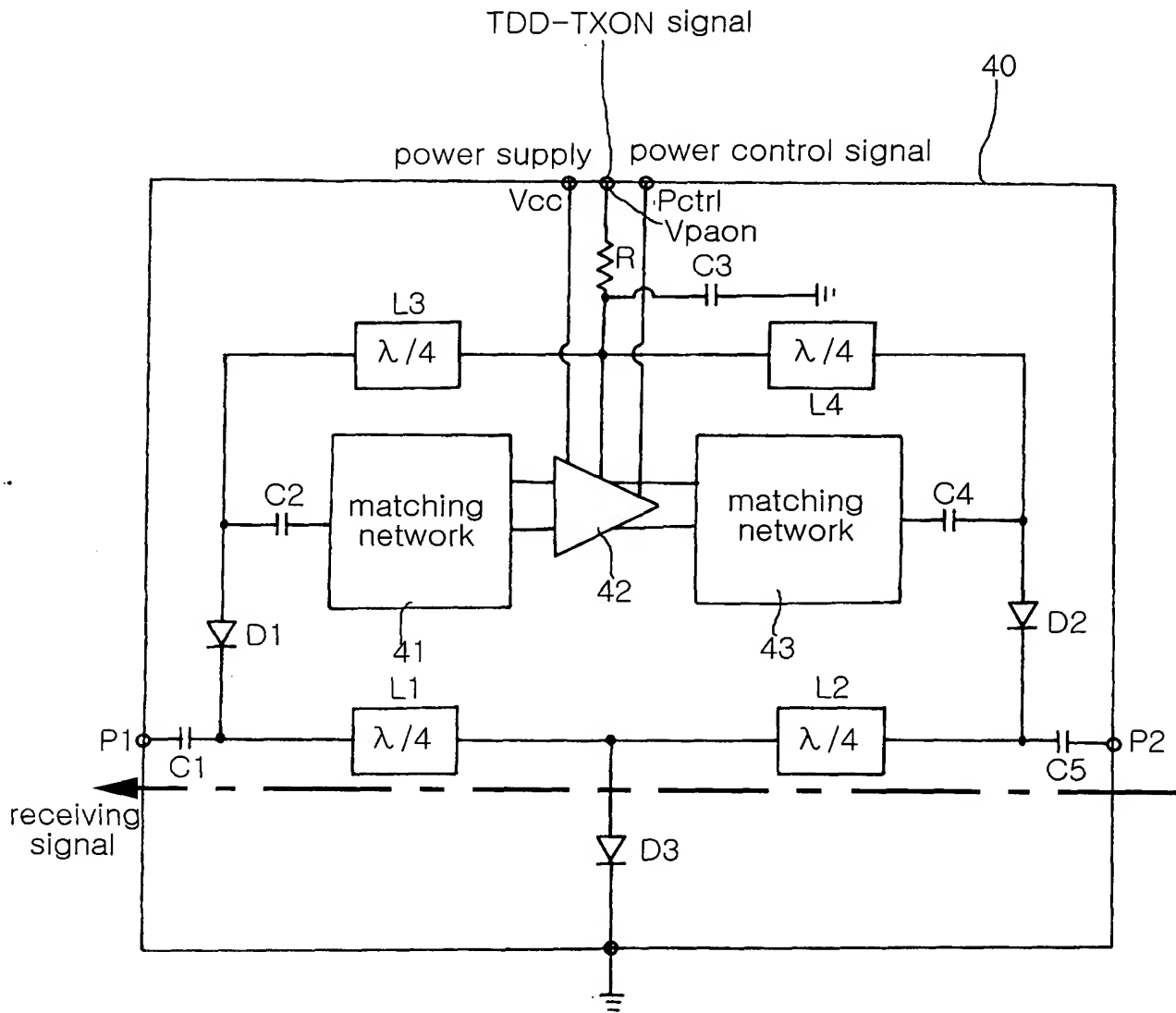


FIG. 6B